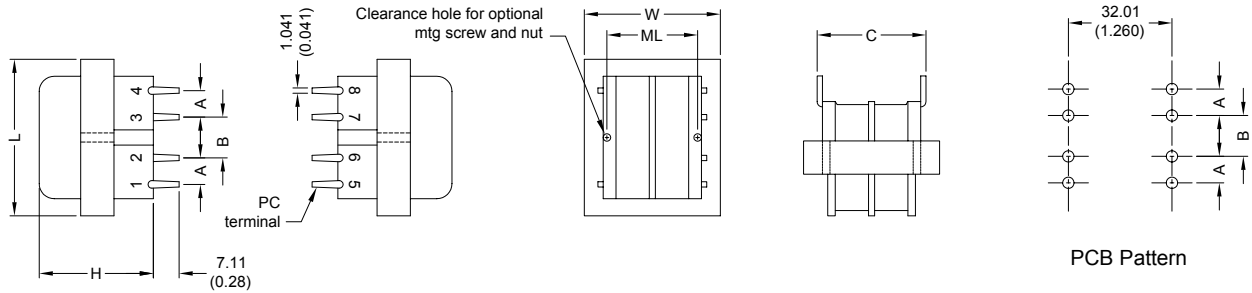


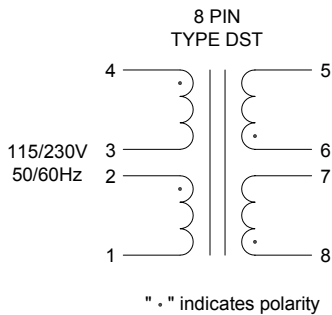
### 1. CONFIGURATION & DIMENSIONS :



UNIT : mm (inch)

SIZE	VA	L	W	H	ML	A	B	C	Optional mtg. screw & nut*	gram
4	6	41.28 (1.625)	33.35 (1.313)	33.35 (1.313)	27.00 (1.063)	6.35 (.250)	8.89 (.350)	32.51 (1.280)	101.6-1016.0 x 34.93 (4-40 x 1.375)	190

### 2. SCHEMATIC :



### 3. ELECTRICAL CHARACTERISTICS ( @ 25°C ) :

- |                                      |                              |
|--------------------------------------|------------------------------|
| a. Primary Voltage                   | AC 115/230 V 50/60 Hz .      |
| b. No Load Primary Current           | Less Than 20mA .             |
| Input AC 115V 60Hz .                 | Less Than 20mA .             |
| Input AC 230V 50Hz .                 | Series AC 19.40V .           |
| c. Secondary Voltage (±5%)           | Parallel AC 9.70V .          |
| d. Secondary Load Current            | Series 0.4A .                |
| e. Full Load Primary Current         | Parallel 0.8A .              |
| Input AC 115V 60Hz .                 | Less Than 45mA .             |
| Input AC 230V 50Hz .                 | Less Than 85mA .             |
| f. Full Load Secondary Voltage (±5%) | Series 16.00V .              |
| g. DC Resistance                     | Parallel 8.00V .             |
| Pin 1-2                              | Primary = 285 Ohm .          |
| Pin 3-4                              | = 350 Ohm .                  |
| Pin 5-6                              | Secondary = 1.98 Ohm .       |
| Pin 7-8                              | = 2.37 Ohm .                 |
| h. Insulation Resistance             | DC 500V 100Meg Ohm Of More . |
| i. Withstand Voltage (Hi-Pot)        | AC 2500V 60Hz 1 Minutes .    |
| j. Temperature Rise                  | Less Than 60 Deg C .         |
| k. Core Size                         | EI-41 x 17.00 mm .           |



**RoHS Compliant**

NOTE : Specifications subject to change without notice. Please check our website for latest information.

15.01.2008



**SUPERWORLD ELECTRONICS (S) PTE LTD**